

BEST AVAILABLE COPY

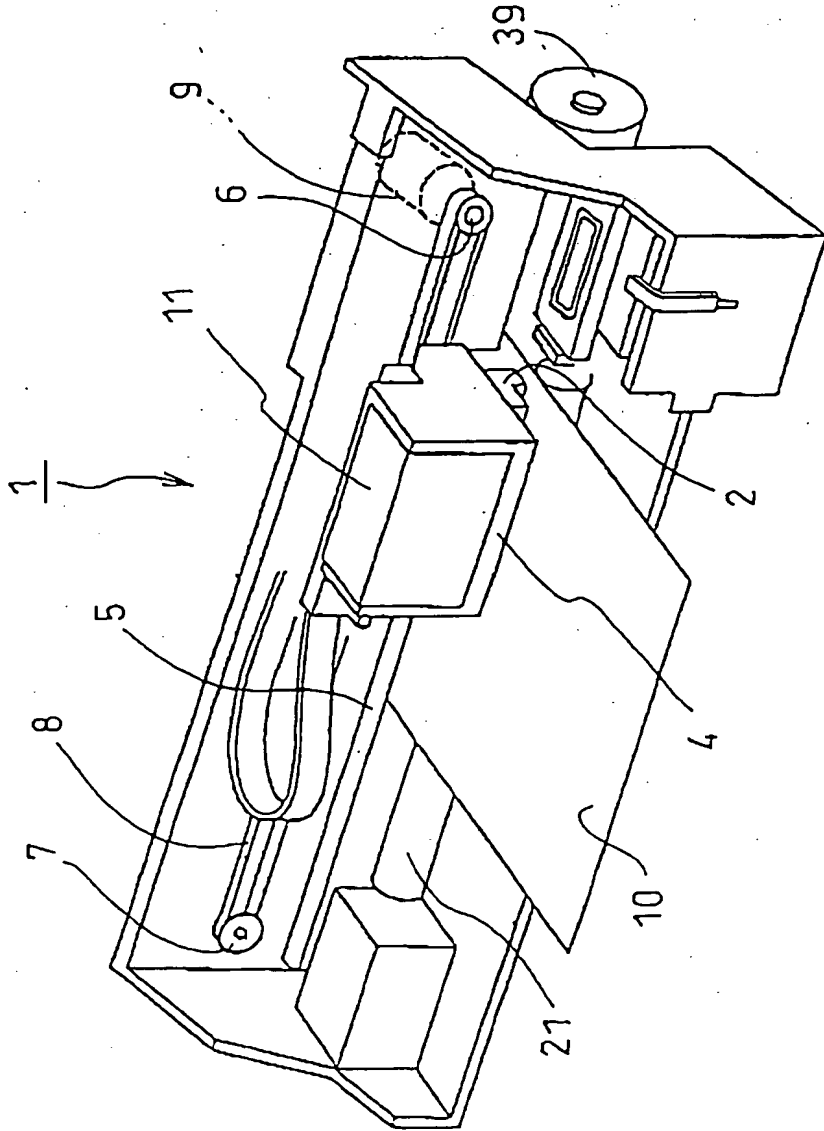
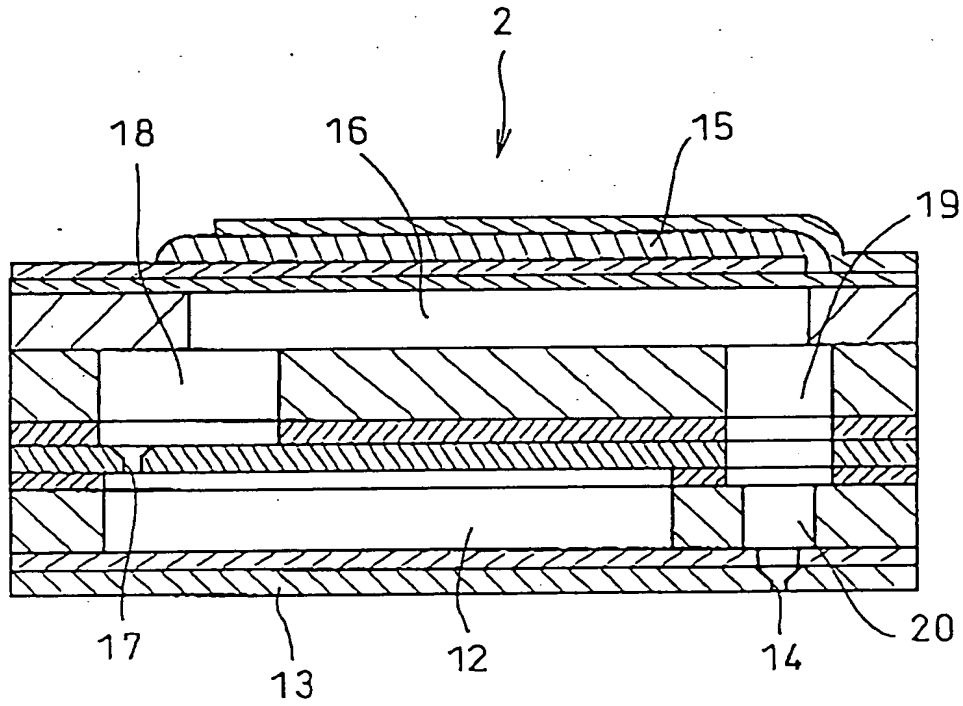


Fig. 1

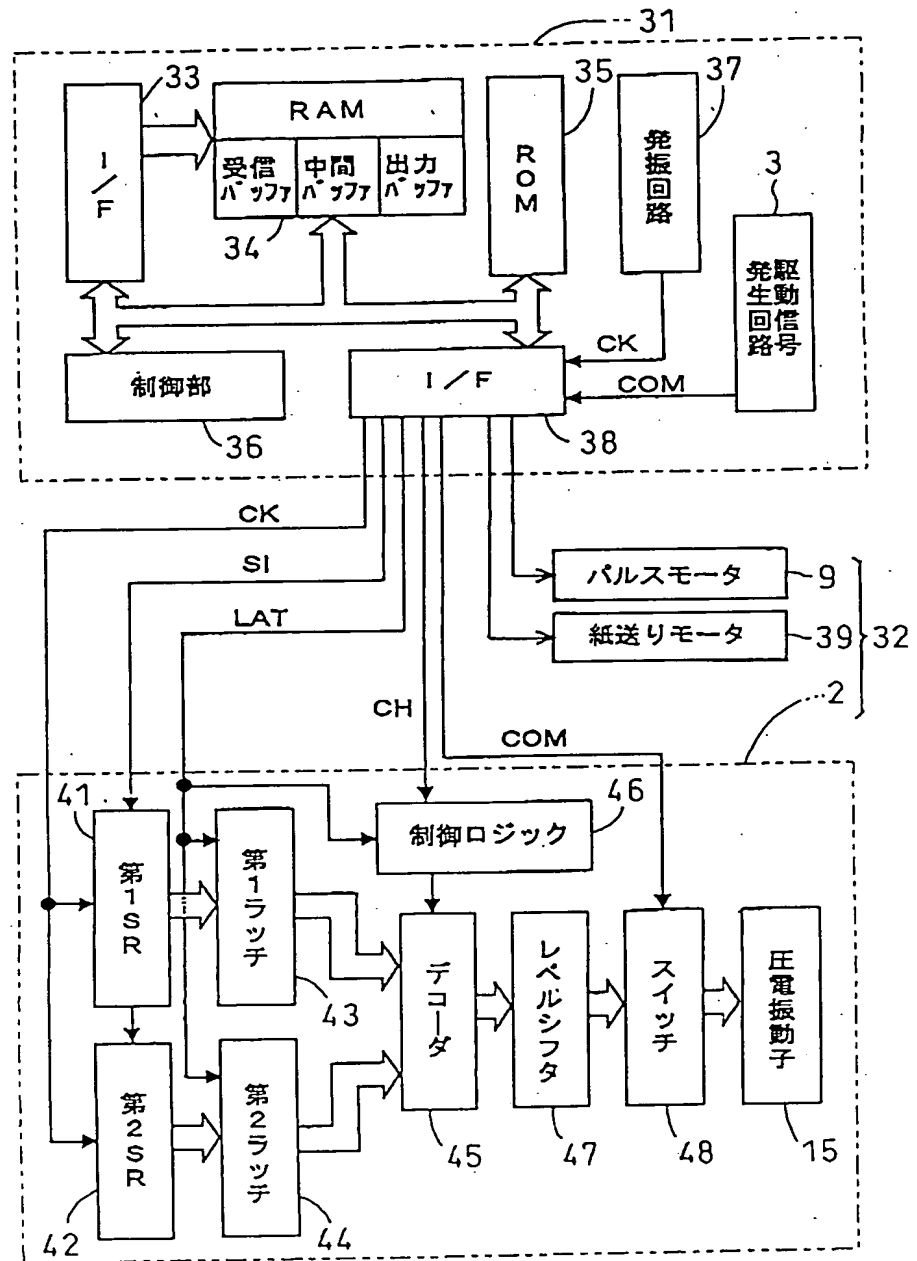
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*Fig. 2*



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Fig. 3

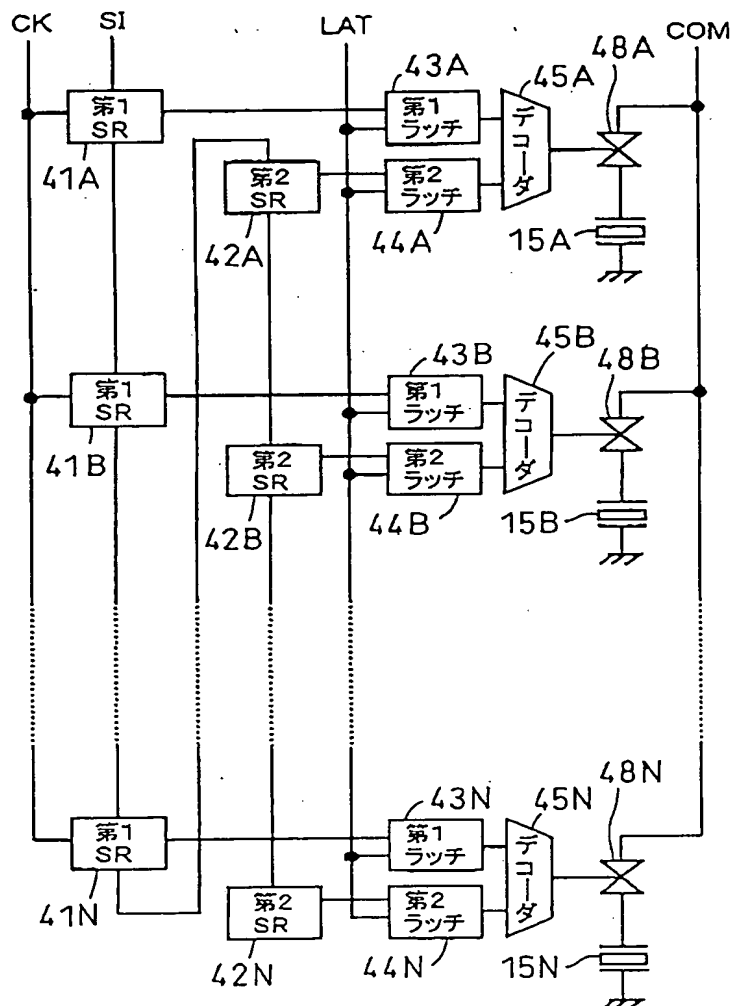


- 3: drive signal generator
- 9: pulse motor
- 15: piezoelectric vibrator
- 33: external I/F
- 34: reception buffer, intermediate buffer, output buffer
- 36: controller
- 37: oscillator
- 38: internal I/F
- 39: paper feeding motor

- 41: first shift register section
- 42: second shift register section
- 43: first latching section
- 44: second latching section
- 45: decoder
- 46: control logic
- 47: level shifter
- 48: switcher

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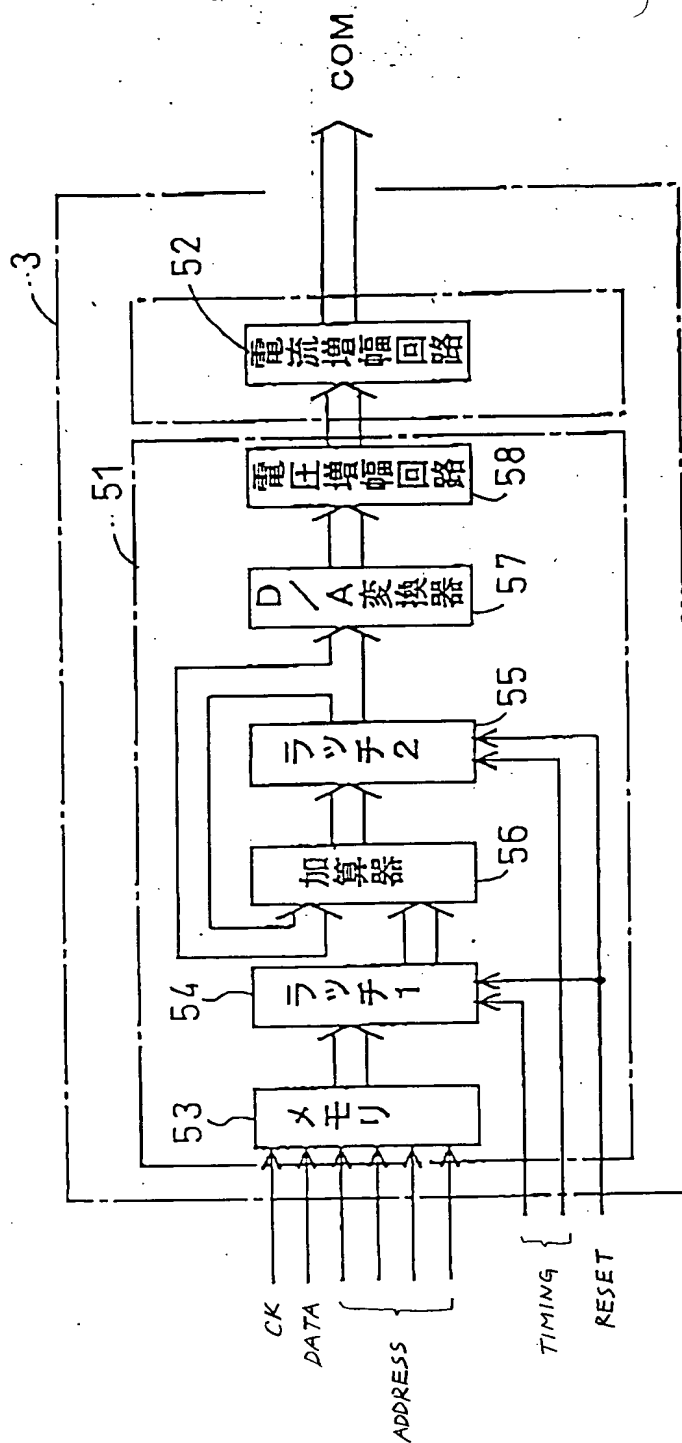
Fig. 4



- 41A ... 41N: first shift register element
- 42A ... 42N: second shift register element
- 43A ... 43N: first latching element
- 44A ... 44N: second latching element
- 45A ... 45N: decoder element

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Fig. 5



- 52: current amplifier
- 53: waveform memory
- 54: first waveform latching section
- 55: second waveform latching section
- 56: adder
- 57: D/A converter
- 58: voltage amplifier

Fig. 6

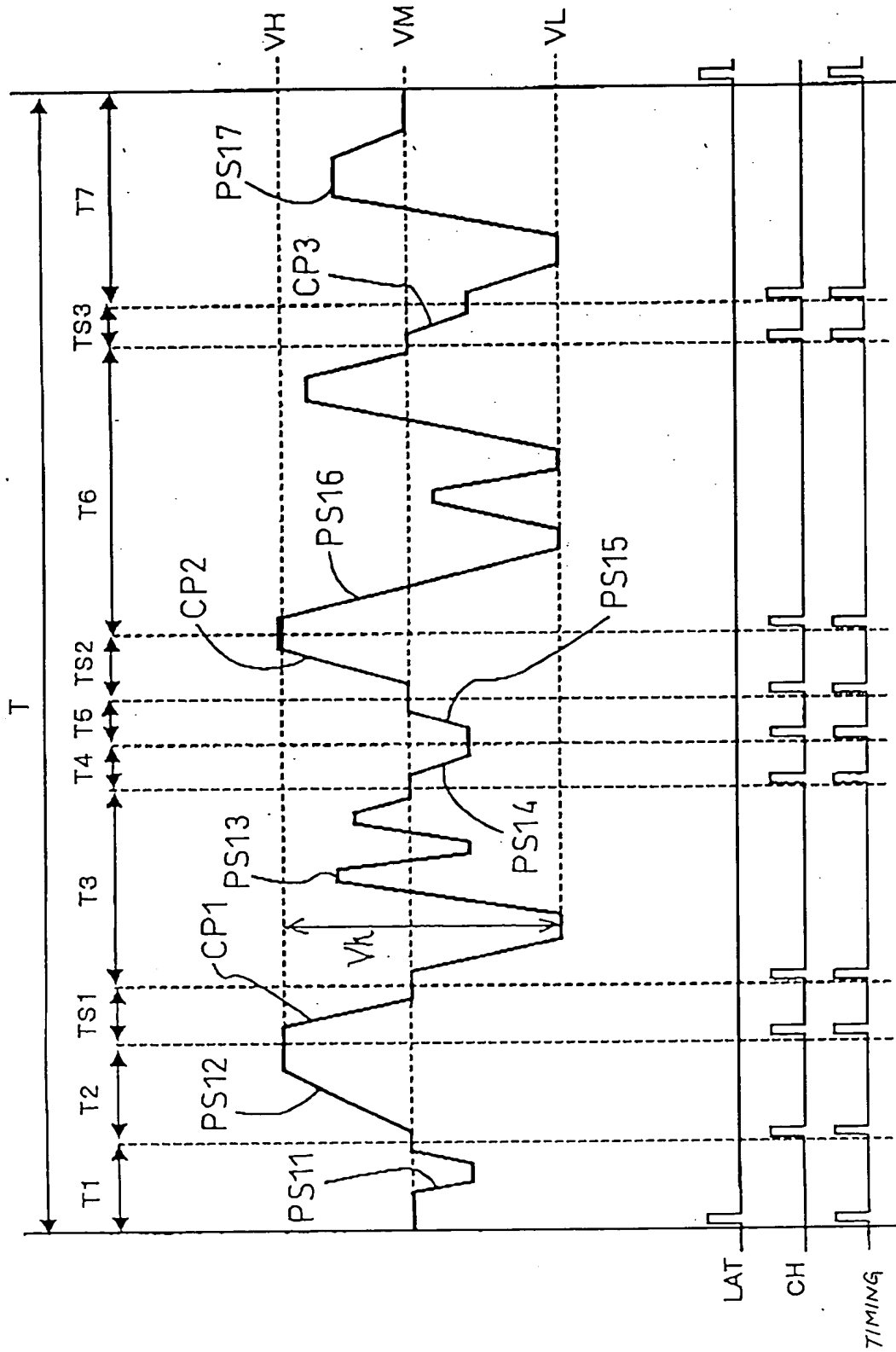
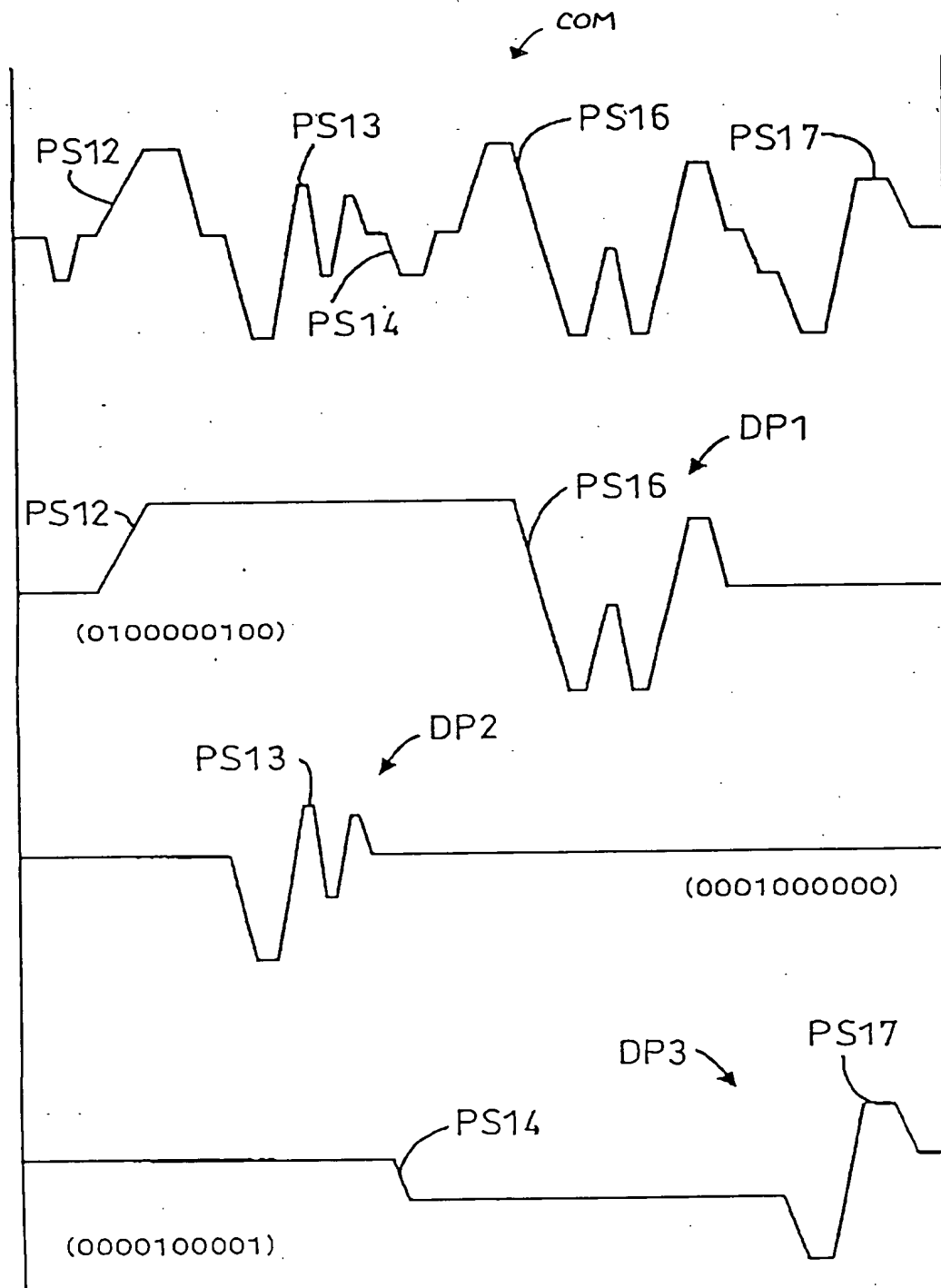


Fig. 7



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Fig. 8

DP1

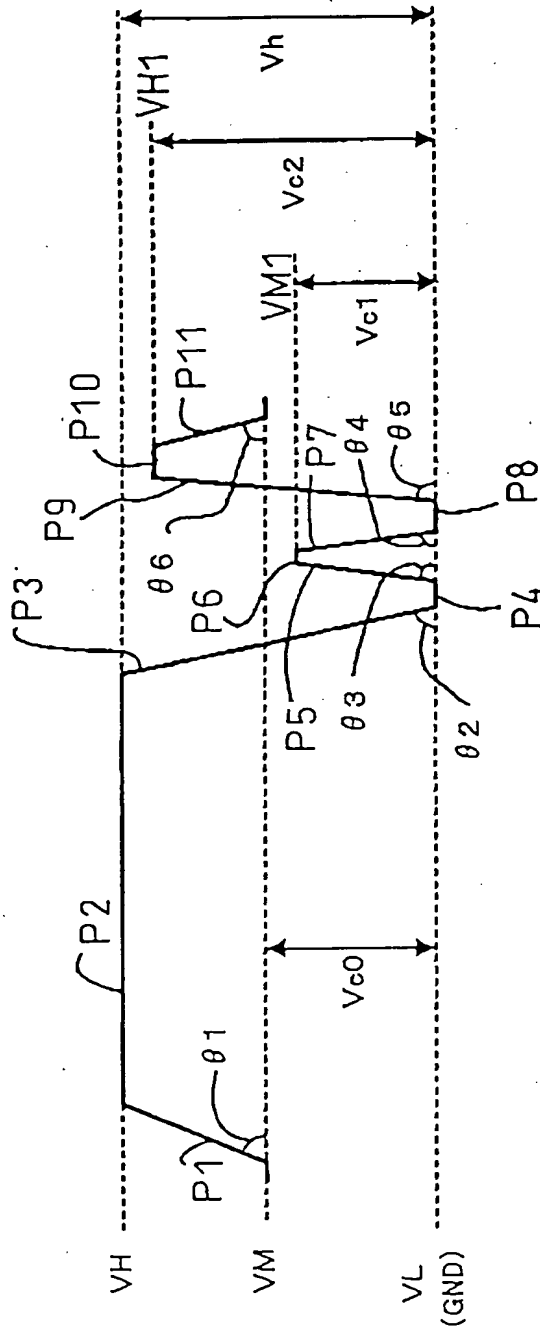




Fig. 9A

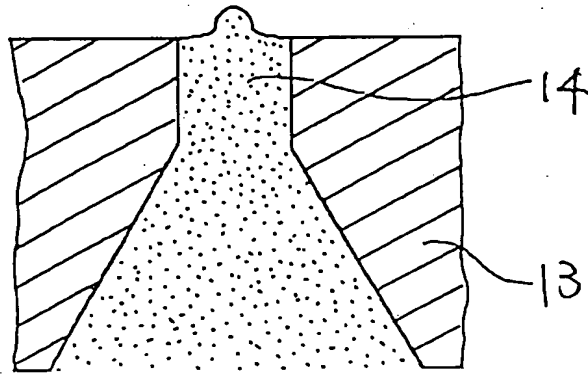


Fig. 9B

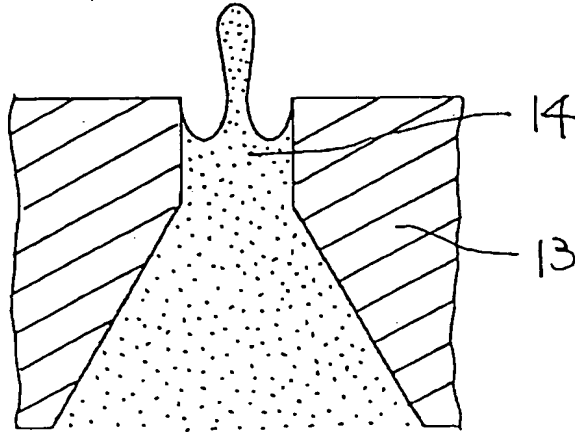
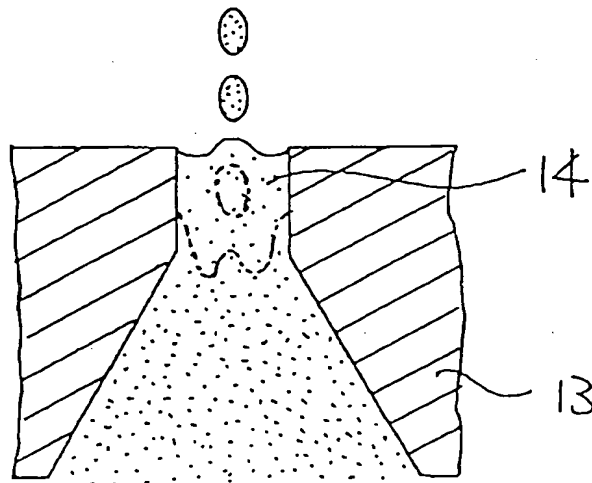


Fig. 9C



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Fig. 10

